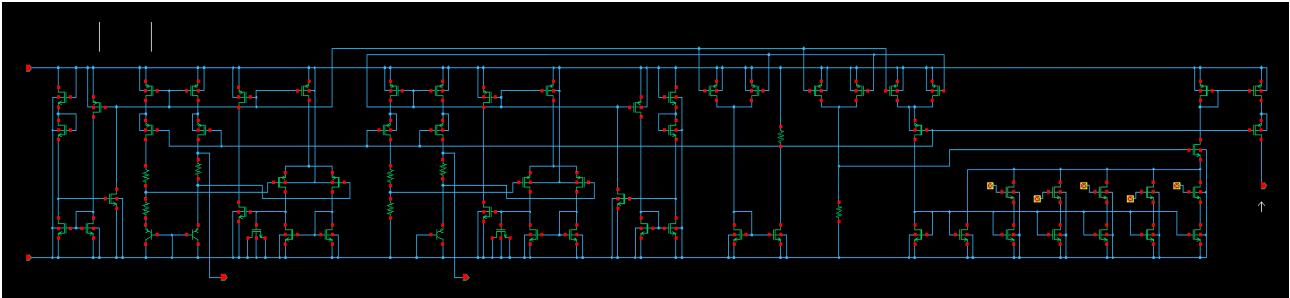




EDA CAD Software Setup Engineer



About Photolitics

We are a team of world-class analog design professionals in providing high-performance analog and mixed-mode circuits for leading custom machine vision CMOS image sensor devices. We have a vast connection network of multi-national companies that shape the market in scientific, medical and industrial machine vision imaging applications. Photolitics is currently looking for highly motivated individuals to help with electronics design setup in their office in Ruse, Bulgaria.

The available position

By joining us in Ruse, you will have the opportunity to gain experience and help us in the setup of electronics design automation CAD software for integrated circuit design. As a technically competent individual in open-source systems you will come across and have the opportunity to develop a custom IC design CAD flow based on open-source tools under the FOSSi umbrella. At Photolitics you will have the choice and opportunity to participate in every phase of custom analog IC design flow setup. The position is available for a fixed duration, with the possibility to turn into a permanent role.

Specific Job Duties

- ◇ Research existing open-source EDA tools for IC design.
- ◇ Develop an operational IC design flow using custom Photolitics in-house and open-source tools.
- ◇ Develop and translate DRC and LVS rule decks for cutting-edge IC process nodes using scripts in Python and Ruby.
- ◇ Develop bash, Perl, and Python scripts for design flow setup under Linux.
- ◇ Write technical documentation on developed software tools.

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Qualifications

- ◇ Highly motivated individual in setup of open-source software often lacking documentation and requiring additional development.
- ◇ Experience and good understanding of the Linux operating system.
- ◇ Exposure to one or more of the following programming languages: Python, C, Perl, bash, Ruby
- ◇ Experience with any of the EDA tools is a plus: Cadence Virtuoso, Mentor IC Station, Calibre, KICAD, Altium Designer, Magic, Electric VLSI, Multisim, Klayout or any other similar EDA tool flow.
- ◇ Understanding of fundamentals in electronics design automation: schematic capture, layout design, Design Rule Checks, Layout Versus Schematic etc.
- ◇ Good communication skills

Other analog design experiences in one or more of the following fields is preferable, but not required

- ◇ Electronics design experience.
- ◇ Basic design skills in FPGA, MCU and embedded systems.

Education

- ◇ BSc in electrical engineering. MSc/PhD or relevant work experience.
- ◇ Candidate can also be a student in his third or final year of studies.

What we offer

- ◇ Stimulating work environment and possibilities for growth into an experienced software and device characterization engineer.
- ◇ Freedom to explore own design solutions. You have the possibility to take complete ownership and contribute to the open-source community.
- ◇ Be encouraged to publish work in highly-ranked scientific journals and have the ability to participate in scientific conferences and training courses.
- ◇ Work with state-of-the-art CMOS fabrication nodes, gain an in-depth knowledge of EDA CAD for IC design.
- ◇ Straight-to-the-point work, no participation in endless nonsense corporate meetings.
- ◇ Work package of 25 paid holidays yearly.
- ◇ Relocation assistance.
- ◇ Competitive salary.

Additional information on the position can be obtained from Deyan Levski at:

deyan.levski@photolitics.com

To apply, please send your application with the usual document (CV), with an example of your prior experience to: deyan.levski@photolitics.com